

EXAMINED BY :	EMERGING DISPLAY TECHNOLOGIES CORPORATION	FILE NO . CAS-0007789
Justin Horng		ISSUE : SEP.03, 2014
APPROVED BY:		TOTAL PAGE : 24
Yung Chang Hu		VERSION : 3

CUSTOMER                      ACCEPTANCE                      SPECIFICATIONS

MODEL NO. :  
  
ET035009DMA  
(RoHS)  
FOR MESSRS :  
\_\_\_\_\_

CUSTOMER'S APPROVAL

DATE : \_\_\_\_\_

BY : \_\_\_\_\_

EMERGING DISPLAY  
TECHNOLOGIES CORPORATION

MODEL NO.	VERSION	PAGE
ET035009DMA	3	0-1

RECORDS OF REVISION	DOC . FIRST ISSUE	AUG.20, 2013
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DATE	REVISED PAGE NO.	SUMMARY
SEP.17, 2013	7	6.1 OPTICAL CHARACTERISTICS THE BRIGHTNESS OF MODULE : MIN.= 250→350, TYP.=300→400
SEP.03, 2014	9	7. OUTLINE DIMENSIONS MARK △ : CHANGE PULL TAPE

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TABLE OF CONTENTS

NO.	ITEM	PAGE
1.	GENERAL SPECIFICATIONS -----	1
2.	MECHANICAL SPECIFICATIONS -----	1
3.	ABSOLUTE MAXIMUM RATINGS -----	2
4.	ELECTRICAL CHARACTERISTICS -----	3
5.	TIMING CHART -----	4 ~ 6
6.	OPTICAL CHARACTERISTICS -----	7, 8
7.	OUTLINE DIMENSIONS -----	9
8.	BLOCK DIMENSIONS -----	10
9.	DETAIL DRAWING OF DOT MATRIX -----	11
10.	INTERFACE SIGNALS -----	12, 13
11.	POWER SUPPLY -----	14
12.	INSPECTION CRITERION -----	15 ~ 24

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## 1. GENERAL SPECIFICATIONS

### 1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER

PLEASE REFER TO :

H X 8 2 3 8 - A

### 1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE)

## 2. MECHANICAL SPECIFICATIONS

(1) DIAGONALS	-----	3.5 inch
(2) NUMBER OF DOTS	-----	320W * (RGB) * 240H DOTS
(3) MODULE SIZE	-----	76.8W * 63.8H * 3.3(D) mm (WITHOUT FPC)
(4) ACTIVE AREA	-----	70.08W * 52.56H mm
(5) DOT SIZE	-----	0.073W * 0.219H mm
(6) PIXEL SIZE	-----	0.219W * 0.219H mm
(7) LCD TYPE	-----	TFT , TRANSMISSIVE
(8) COLOR	-----	262K
(9) VIEWING DIRECTION	-----	SUPER WIDE VIEW
(10) BACK LIGHT	-----	LED , COLOR : WHITE
(11) INTERFACE MODE	-----	RGB 24BIT PARALLEL (DE/SYNC MODE)

### 3. ABSOLUTE MAXIMUM RATINGS

#### 3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER VOLTAGE	VDD	VSS-0.3	5.0	V	—
	VCC	-0.3	4.0	V	—
LED BACKLIGHT DISSIPATION	PD	—	540	mW	—
LED BACKLIGHT CURRENT	IF	—	25	mA	—
LED BACKLIGHT REVERSE VOLTAGE	VR	—	30	V	—

#### 3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE ( 1 ), ( 2 )
HUMIDITY	NOTE ( 3 )		NOTE ( 3 )		WITHOUT CONDENSATION
VIBRATION	—	2.45m/s <sup>2</sup> ( 0.25G )	—	11.76m/s <sup>2</sup> ( 1.2G )	5~20Hz , 1HR 20~500Hz(20Hz) , 1HR 20~500Hz(500Hz) , 1HR X,Y,Z,TOTAL 3HR
SHOCK	—	29.4m/s <sup>2</sup> ( 3G )	—	490m/s <sup>2</sup> ( 50G )	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE ( 1 ) : Ta AT -30°C : 48HR MAX.  
80°C : 168HR MAX.

NOTE ( 2 ) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE ( 3 ) : Ta ≤ 60°C : 90%RH MAX (96HRS MAX).

Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C (96HRS MAX).

4. ELECTRICAL CHARACTERISTICS

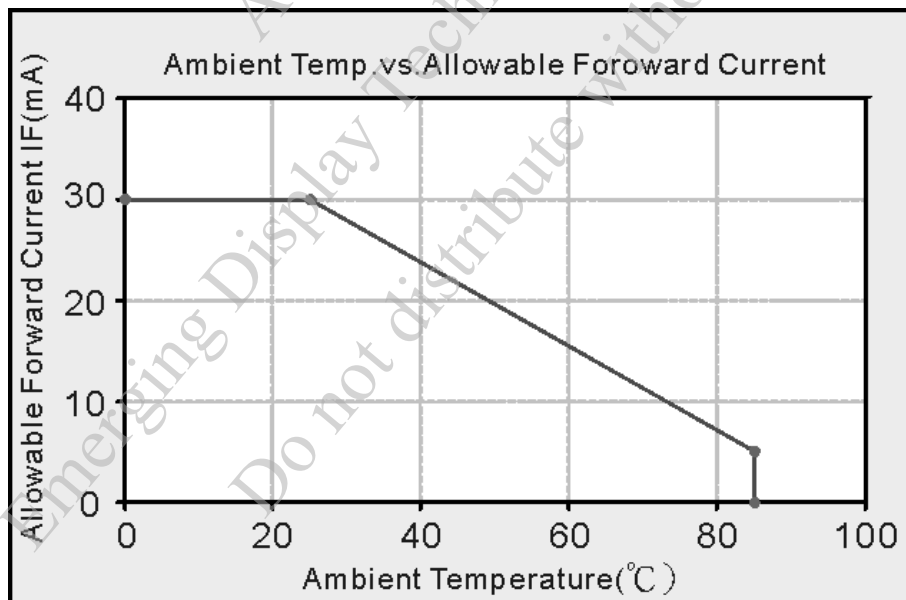
Ta = 25 °C

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
DIGITAL POWER SUPPLY	VCC	—	2.5	3.3	3.6	V	
ANALOG POWER SUPPLY	VDD	—	2.5	3.3	3.6	V	
DIGITAL OPERATING CURRENT	ICC	—	—	1	2	mA	
ANALOG OPERATING CURRENT	IDD	—	—	10	15	mA	
LOGIC HIGH OUTPUT VOLTAGE	VOH	IOUT=-100µA	0.9*VCC	—	VCC	V	
LOGIC LOW OUTPUT VOLTAGE	VOL	IOUT=100µA	0	—	0.1*VCC	V	
LOGIC HIGH INPUT VOLTAGE	VIH	H LEVEL	0.8*VCC	—	VCC	V	
LOGIC LOW INPUT VOLTAGE	VIL	L LEVEL	0	—	0.2*VCC	V	
POWER SUPPLY FOR LED BACKLIGHT	V <sub>F</sub>	IF=20mA	18	19.8	21.6	V	NOTE (1)
LED LIFE TIME		—	30k	40k	—	hr	

NOTE (1) : INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



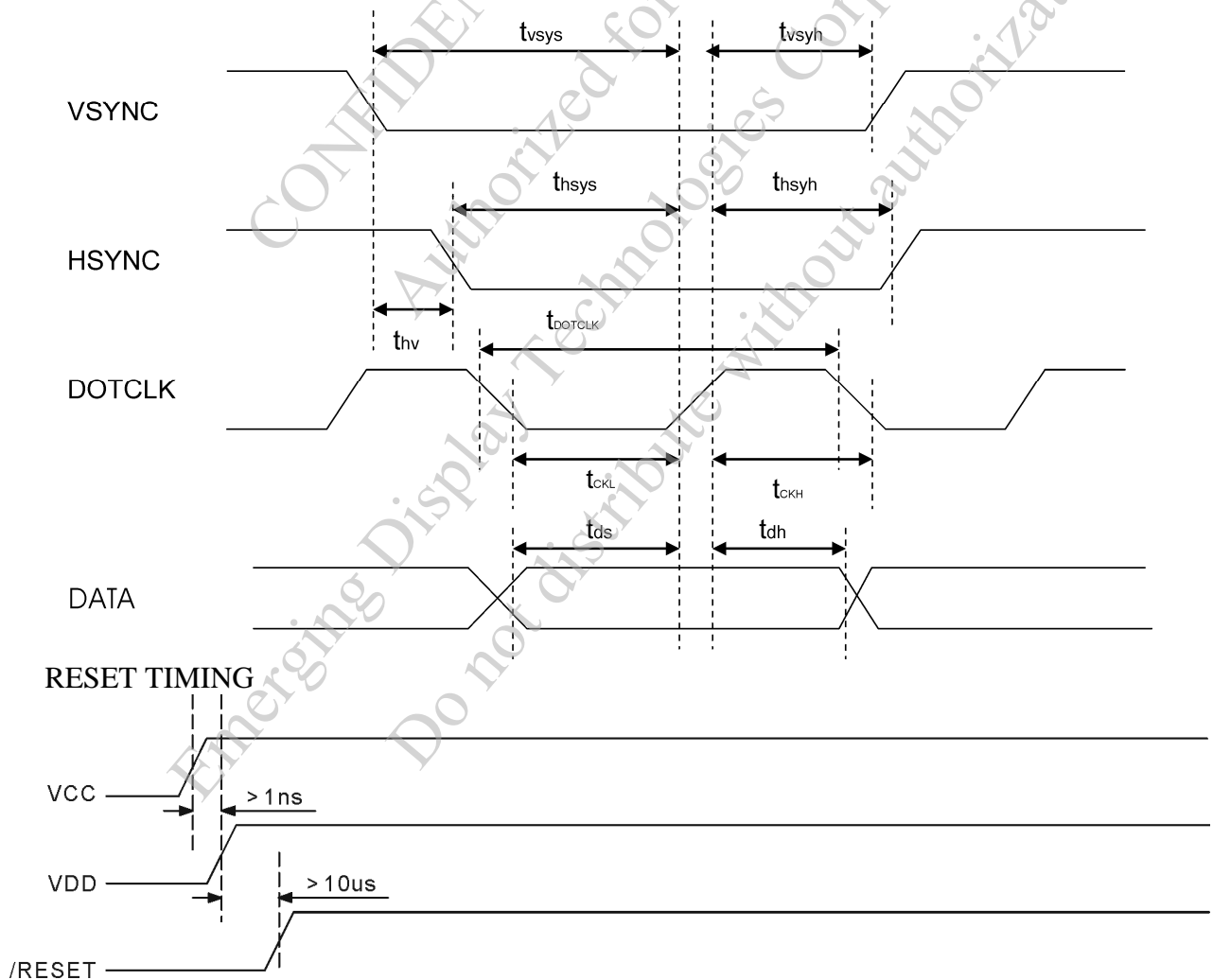
NOTE (2) : AMBIENT TEMP .VS. ALLOWABLE FORWARD CURRENT.(PER LED)



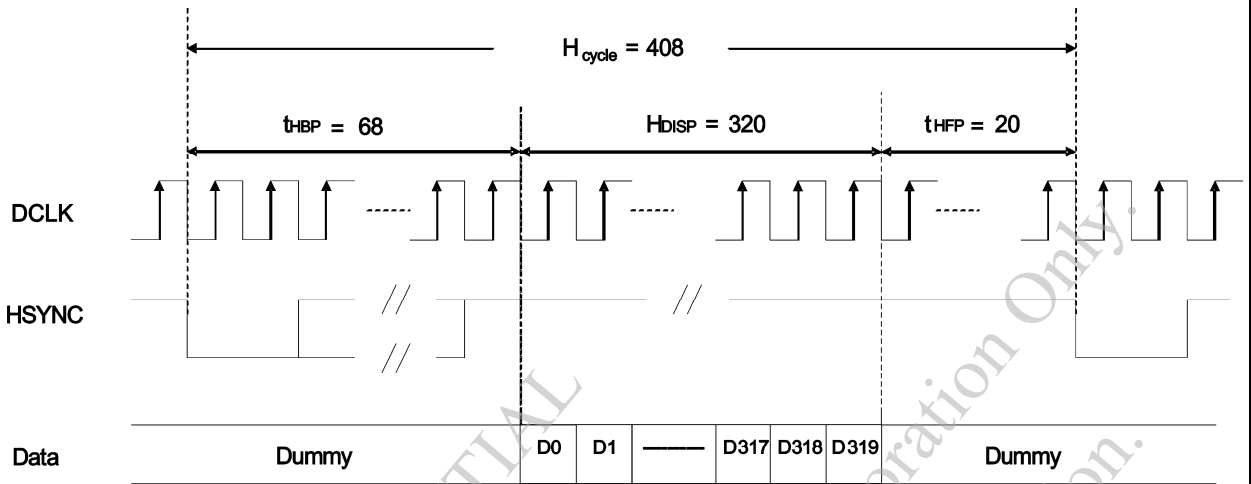
5. TIMING CHART

5.1 PIXEL TIMING

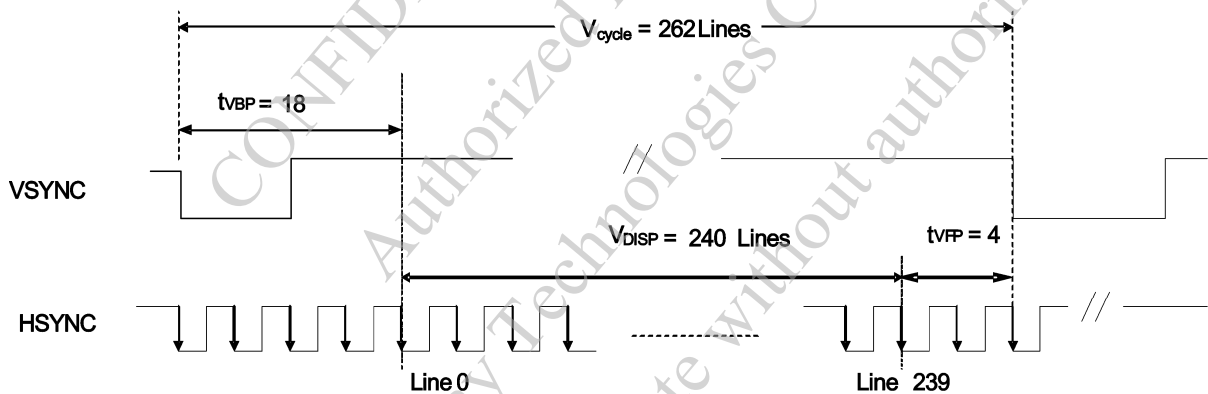
CHARACTERISTICS	SYMBOL	MIN.	TYP.	MAX.	UNIT
DOTCLK FREQUENCY	fDOTCLK	—	6.5	10	MHz
DOTCLK PERIOD	tDOTCLK	100	154	—	ns
VERTICAL SYNC SETUP TIME	tvsys	20	—	—	ns
VERTICAL SYNC HOLD TIME	tvsyh	20	—	—	ns
HORIZONTAL SYNC SETUP TIME	thsys	20	—	—	ns
HORIZONTAL SYNC HOLD TIME	thsyh	20	—	—	ns
PHASE DIFFERENCE OF SYNC SIGNAL FALLING EDGE	thv	1	—	240	tDOTCLK
DOTCLK LOW PERIOD	tCKL	50	—	—	ns
DOTCLK HIGH PERIOD	tCKH	50	—	—	ns
DATA SETUP TIME	tds	12	—	—	ns
DATA HOLD TIME	tdh	12	—	—	ns
RESET PULSE WIDTH	tRES	10	—	—	μs



5.2 PARALLEL RGB INTERFACE (SYNC MODE)



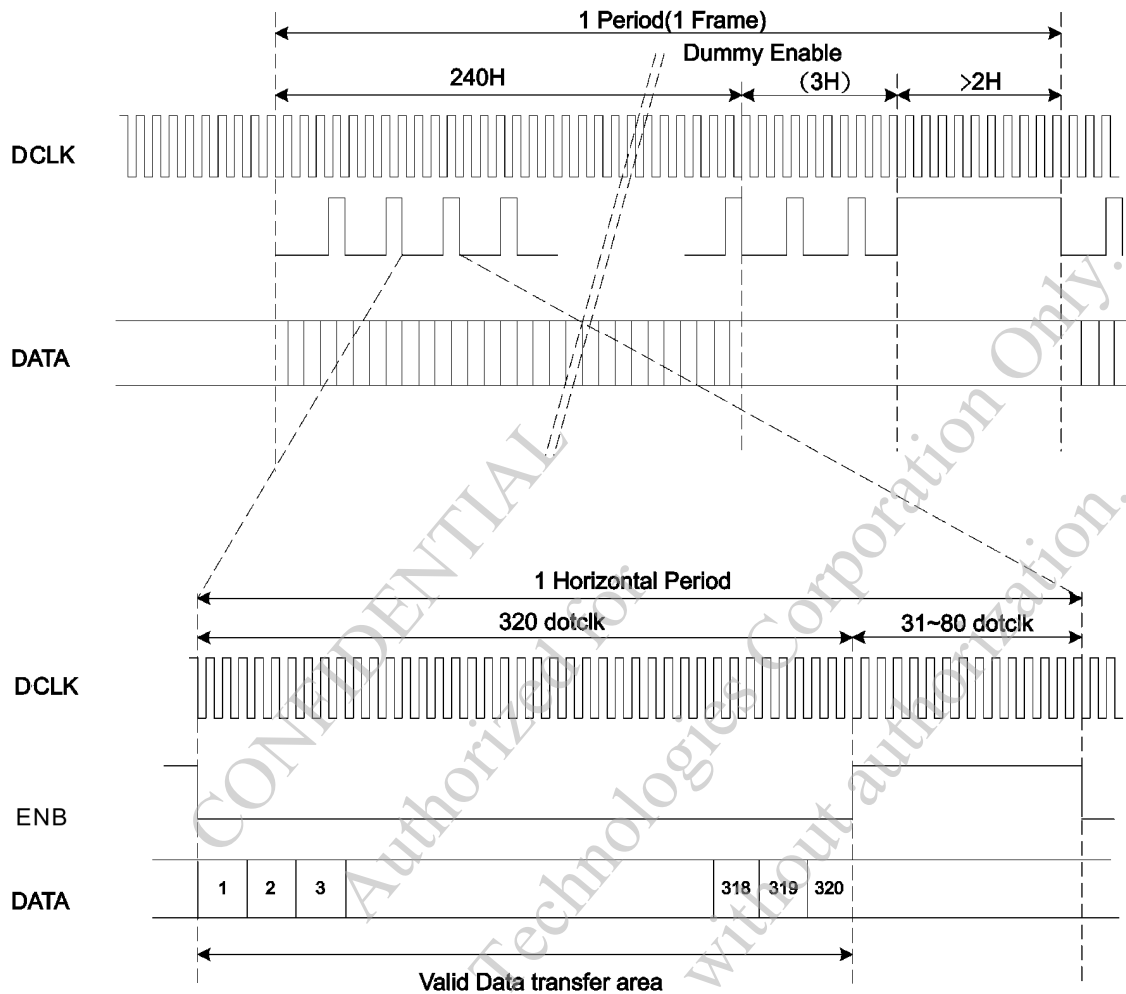
a) Horizontal Data Transaction Timing



b) Vertical Data Transaction Timing



5.3 LCD MODULE PARALLEL RGB INTERFACE (DE MODE)



CHARACTERISTICS	SYMBOL	MIN.	TYP.	MAX.	
DCLK FREQUENCY	f <sub>DCLK</sub>	—	6.5	10	MHz
DCLK PERIOD	t <sub>DCLK</sub>	100	154	—	ns
HORIZONTAL FREQUENCY (LINE)	f <sub>H</sub>	—	15.72	22.35	KHz
VERTICAL FREQUENCY (REFRESH)	f <sub>V</sub>	—	60	90	Hz
HORIZONTAL BACK PORCH	t <sub>HBP</sub>	—	68	—	t <sub>DCLK</sub>
HORIZONTAL FRONT PORCH	t <sub>HFP</sub>	—	20	—	t <sub>DCLK</sub>
HORIZONTAL DATA START POINT	t <sub>HBP</sub>	—	68	—	t <sub>DCLK</sub>
HORIZONTAL BLANKING PERIOD	t <sub>HBP</sub> + t <sub>HFP</sub>	—	88	—	t <sub>DCLK</sub>
HORIZONTAL DISPLAY AREA	H <sub>DISP</sub>	—	320	—	t <sub>DCLK</sub>
HORIZONTAL CYCLE	H <sub>cycle</sub>	—	408	450	t <sub>DCLK</sub>
VERTICAL BACK PORCH	t <sub>VBP</sub>	—	18	—	Lines
VERTICAL FRONT PORCH	t <sub>VFP</sub>	—	4	—	Lines
VERTICAL DATA START POINT	t <sub>VBP</sub>	—	18	—	Lines
VERTICAL BLANKING PERIOD	t <sub>VBP</sub> + t <sub>VFP</sub>	—	22	—	Lines
VERTICAL DISPLAY AREA	NTSC	—	240	—	Lines
	PAL	—	280(PALM=0)	—	
VERTICAL CYCLE	NTSC	—	262	350	Lines
	PAL	—	313		

6. OPTICAL CHARACTERISTICS (NOTE1)

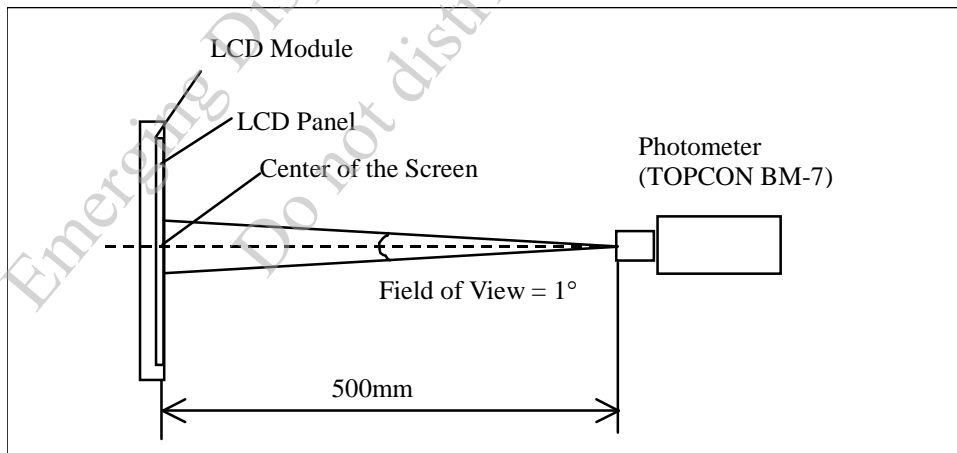
6.1 OPTICAL CHARACTERISTICS

Ta = 25 ± 2°C

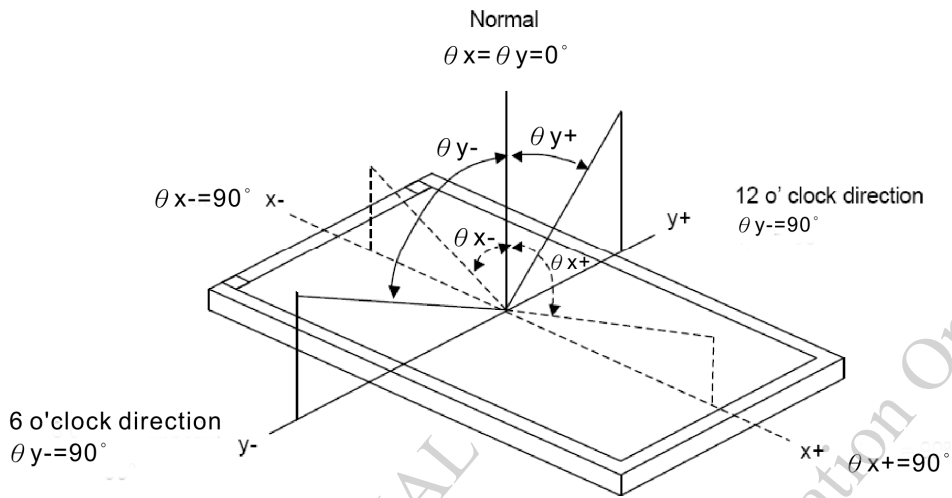
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK	
VIEWING ANGLE	$\theta_{y+}$	CR ≥ 10	$\theta_x=0^\circ$	70	80	—	deg.	(2), (3)
	$\theta_{y-}$			70	80	—		
	$\theta_{x+}$		$\theta_y=0^\circ$	70	80	—		
	$\theta_{x-}$			70	80	—		
CONTRAST RATIO	CR	$\theta_x=0^\circ, \theta_y=0^\circ$	250	350	—	—	(3)	
RESPONSE TIME	T <sub>R</sub> (rise)	$\theta_x=0^\circ, \theta_y=0^\circ$	—	15	20	msec	(4)	
	T <sub>F</sub> (fall)		—	35	50			
THE BRIGHTNESS OF MODULE	B	$\theta_x=0^\circ, \theta_y=0^\circ$ IF=20mA	350	400	—	cd/m <sup>2</sup>	(5)	
COLOR OF CIE COORDINATE	WHITE	$\theta_x=0^\circ, \theta_y=0^\circ$ IF=20mA NTSC : 60%	W <sub>x</sub>	0.260	0.310	0.360	—	(6)
			W <sub>y</sub>	0.290	0.340	0.390		
	RED		R <sub>x</sub>	0.562	0.612	0.662		
			R <sub>y</sub>	0.305	0.355	0.405		
	GREEN		G <sub>x</sub>	0.280	0.330	0.380		
			G <sub>y</sub>	0.540	0.590	0.640		
	BLUE		B <sub>x</sub>	0.090	0.140	0.190		
			B <sub>y</sub>	0.020	0.070	0.120		
THE UNIFORMITY OF BRIGHTNESS	—	—	70	—	—	%	—	

NOTE (1) : TEST EQUIPMENT SETUP :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



NOTE (2) : DEFINITION OF VIEWING ANGLE :

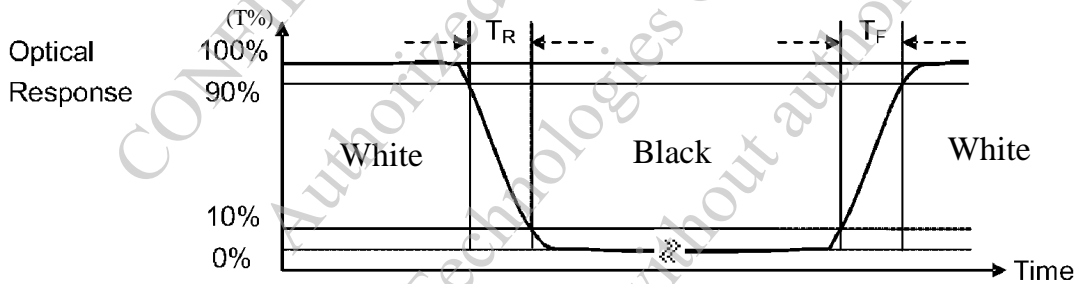


NOTE (3) : DEFINITION OF CONTRAST RATIO :

$$\text{CONTRAST RATIO (CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

NOTE (4) : DEFINITION OF RESPONSE TIME : TR AND TF

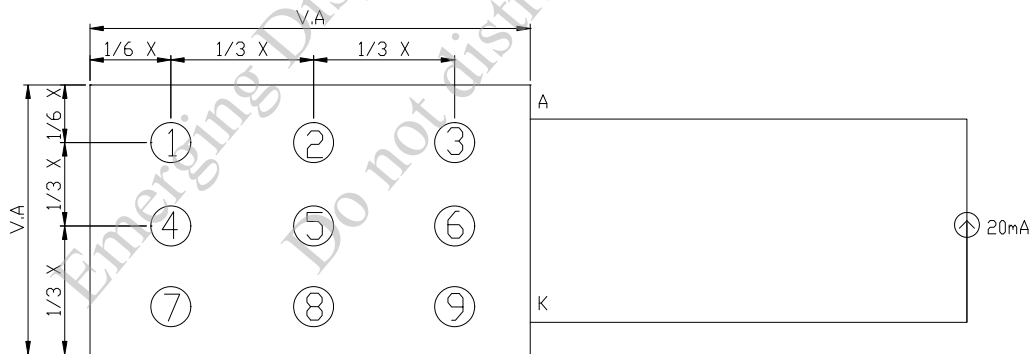
THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5) : BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

NOTE (6) : THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

## 6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY

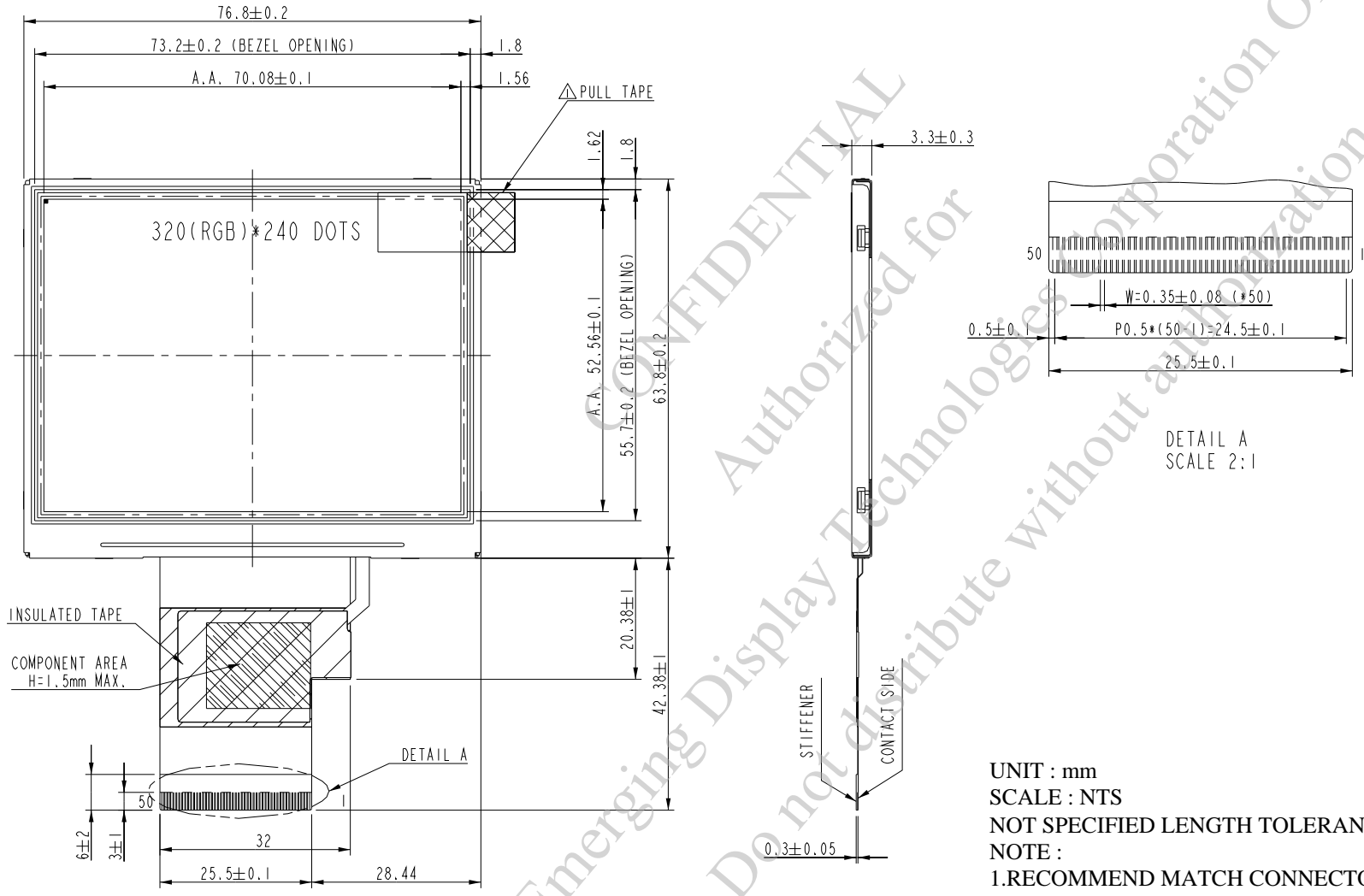


UNIT : mm

## 6.3 THE CALCULATING METHOD OF UNIFORMITY

$$\text{UNIFORMITY} = \left[ 1 - \frac{\text{MAXIMUM BRIGHTNESS} - \text{MINIMUM BRIGHTNESS}}{\text{AVERAGE BRIGHTNESS}} \right] \times 100\%$$

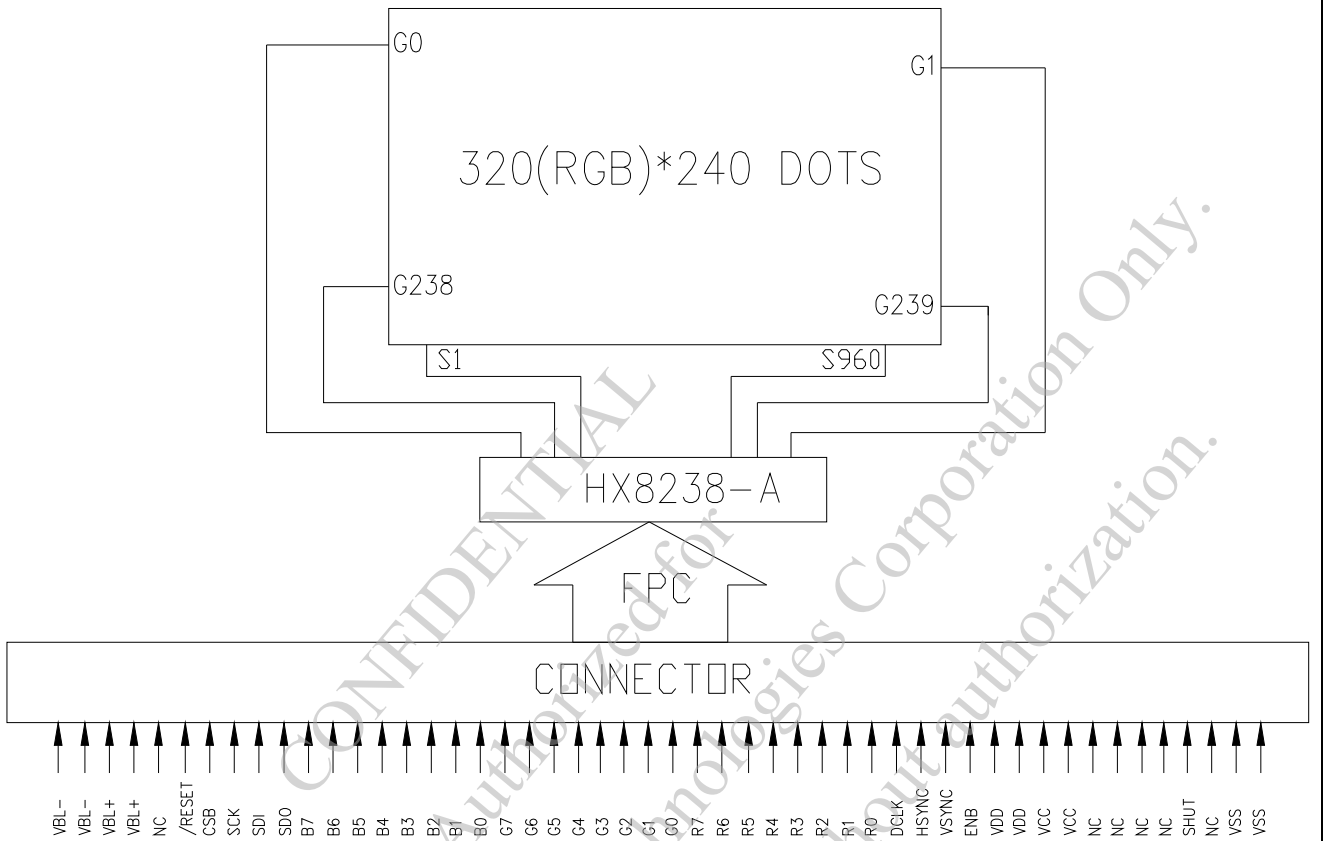
7. OUTLINE DIMENSIONS



UNIT : mm  
SCALE : NTS  
NOT SPECIFIED LENGTH TOLERANCE IS ± 0.5

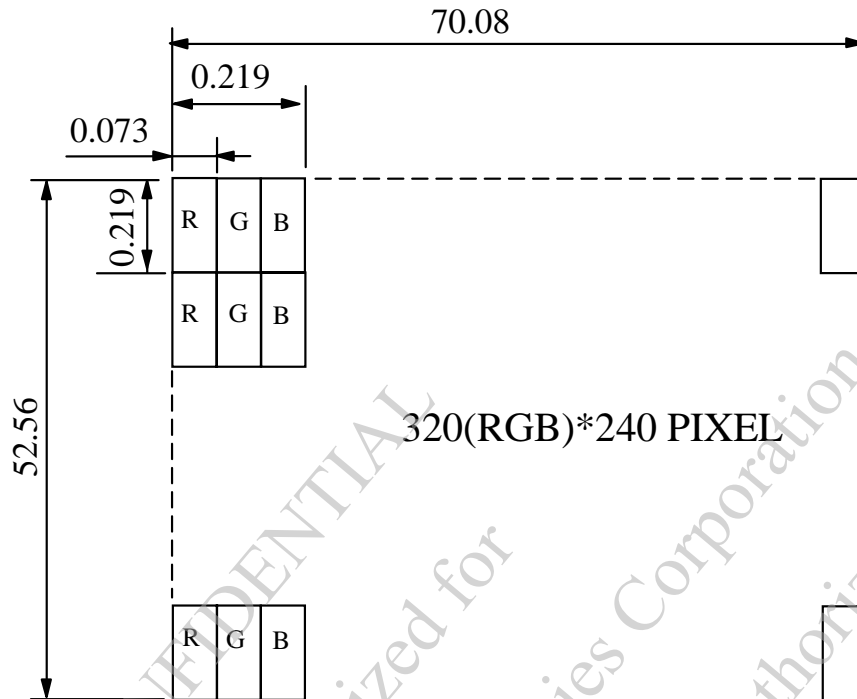
- NOTE :
- 1.RECOMMEND MATCH CONNECTOR KYOCERA:04 6299 050 SERIES
  - 2.MARK △ MODIFY (NUMBER NOTE MODIFY VERSION)

8. BLOCK DIMENSION



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9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm  
SCALE : NTS  
NOT SPECIFIED TOLERANCE IS  $\pm 0.1$   
DOTS MATRIX TOLERANCE IS  $\pm 0.01$

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10. INTERFACE SIGNALS

PIN NO.	SYMBOL	I/O	FUNCTION
1	VBL-	P	BACKLIGHT LED GROUND (K)
2	VBL-	P	BACKLIGHT LED GROUND (K)
3	VBL+	P	BACKLIGHT LED POWER (A)
4	VBL+	P	BACKLIGHT LED POWER (A)
5	NC	—	NOT USE
6	/RESET	I	HARDWARE RESET
7	CSB	I	SPI INTERFACE CHIP SELECT BAR
8	SCK	I	SPI INTERFACE DATA CLOCK
9	SDI	I	SPI INTERFACE DATA (INPUT)
10	SDO	O	SPI INTERFACE DATA(OUTPUT)
11	B7	I	BLUE DATA BIT 7
12	B6	I	BLUE DATA BIT 6
13	B5	I	BLUE DATA BIT 5
14	B4	I	BLUE DATA BIT 4
15	B3	I	BLUE DATA BIT 3
16	B2	I	BLUE DATA BIT 2
17	B1	I	BLUE DATA BIT 1
18	B0	I	BLUE DATA BIT 0
19	G7	I	GREEN DATA BIT 7
20	G6	I	GREEN DATA BIT 6
21	G5	I	GREEN DATA BIT 5
22	G4	I	GREEN DATA BIT 4
23	G3	I	GREEN DATA BIT 3
24	G2	I	GREEN DATA BIT 2
25	G1	I	GREEN DATA BIT 1
26	G0	I	GREEN DATA BIT 0
27	R7	I	RED DATA BIT 7
28	R6	I	RED DATA BIT 6
29	R5	I	RED DATA BIT 5
30	R4	I	RED DATA BIT 4
31	R3	I	RED DATA BIT 3
32	R2	I	RED DATA BIT 2
33	R1	I	RED DATA BIT 1
34	R0	I	RED DATA BIT 0

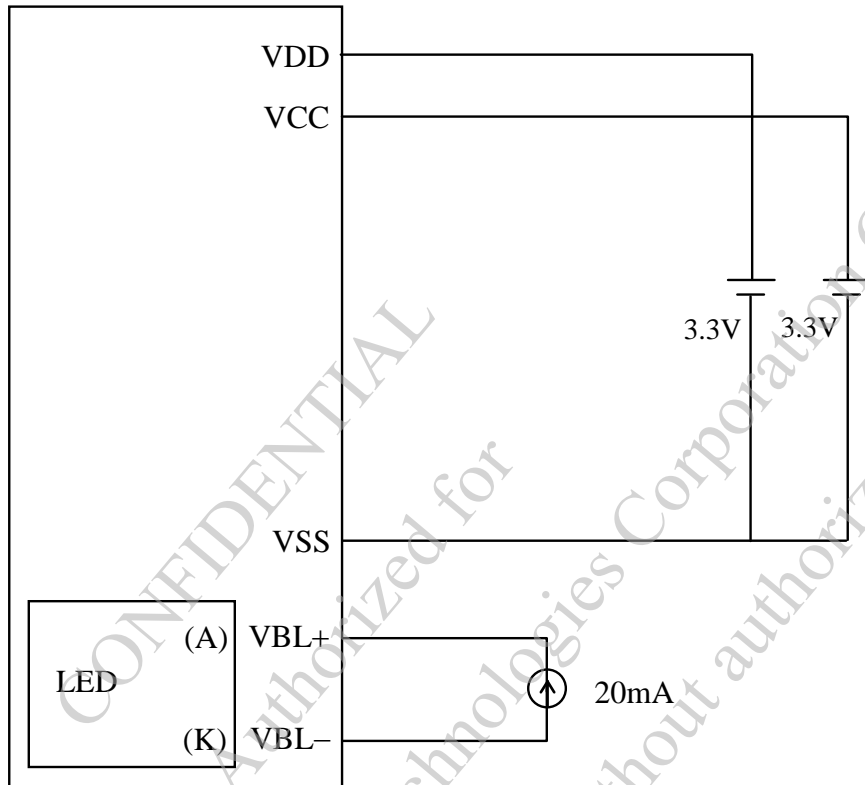
PIN NO.	SYMBOL	I/O	FUNCTION
35	DCLK	I	DOT DATA COLCK
36	HSYNC	I	HORIZONTAL SYNC INPUT
37	VSYNC	I	VERTICAL SYNC INPUT
38	ENB	I	DATA ENABLE INPUT
39	VDD	P	ANALOG POWER
40	VDD	P	ANALOG POWER
41	VCC	P	DIGITAL POWER
42	VCC	P	DIGITAL POWER
43	NC	—	NOT USE
44	NC	—	NOT USE
45	NC	—	NOT USE
46	NC	—	NOT USE
47	SHUT	I	DISPLAY SHUT DOWN PIN TO PUT THE DRIVER INTO SLEEP MODE. CONNECT TO VCC FOR SLEEP MODE. CONNECT TO VSS FOR NORMAL OPERATING MODE.
48	NC	—	NOT USE
49	VSS	P	GROUND
50	VSS	P	GROUND

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11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



12. INSPECTION CRITERION

12.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.( E.D.T ) TO CUSTOMERS

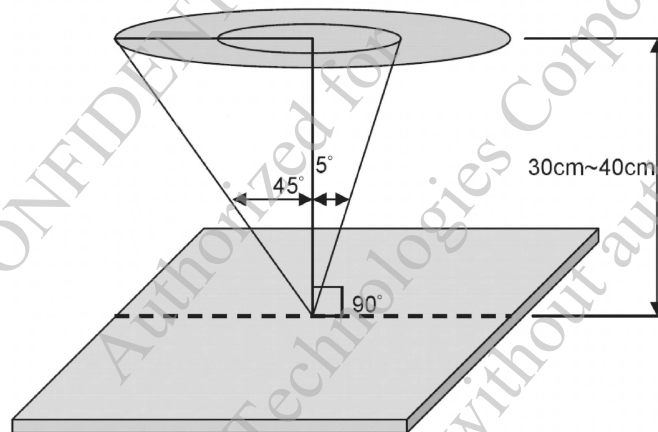
12.2 INSPECTION CONDITIONS

12.2.1 (1)OBSERVATION DISTANCE : 35cm±5cm

(2)VIEW ANGLE :

NON-OPERATION CONDITION : ±5°  
(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION : ±45°  
(PERPENDICULAR TO LCD PANEL SURFACE)



12.2.2 ENVIRONMENT CONDITIONS :

AMBIENT TEMPERATURE		20°C~25°C
AMBIENT HUMIDITY		65±20% RH
AMBIENT ILLUMINATION	COSMETIC INSPECTION	MORE THAN 600Lux
	FUNCTIONAL INSPECTION	300~500 Lux

12.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

12.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD :

MIL-STD-105E

LEVEL II

(b)AQL : MAJOR DEFECT : AQL 0.65

MINOR DEFECT : AQL 1.0

12.3 INSPECTION STANDARDS

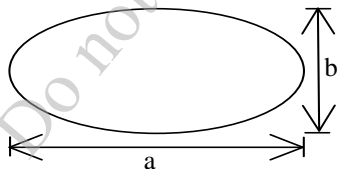
12.3.1 VISUAL DEFECTS CLASSIFICATION

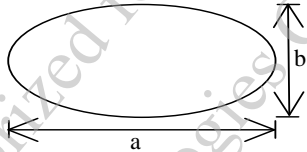
TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	<ul style="list-style-type: none"> <li>• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS</li> <li>EX: DISCONNECTION, SHORT CIRCUIT ETC</li> </ul>	0.65
	2.BACKLIGHT	<ul style="list-style-type: none"> <li>• NO LIGHT</li> <li>• FLICKERING AND OTHER ABNORMAL ILLUMINATION</li> </ul>	
	3.DIMENSIONS	<ul style="list-style-type: none"> <li>• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS</li> </ul>	
MINOR DEFECT	1.DISPLAY ZONE	<ul style="list-style-type: none"> <li>• BLACK/WHITE SPOT</li> <li>• BUBBLES ON POLARIZER</li> <li>• NEWTON RING</li> <li>• BLACK/WHITE LINE</li> <li>• SCRATCH</li> <li>• CONTAMINATION</li> <li>• LEVER COLOR SPREED</li> </ul>	1.0
	2.BEZEL ZONE	<ul style="list-style-type: none"> <li>• STAINS</li> <li>• SCRATCHES</li> <li>• FOREIGN MATTER</li> </ul>	
	3.SOLDERING	<ul style="list-style-type: none"> <li>• INSUFFICIENT SOLDER</li> <li>• SOLDERED IN INCORRECT POSITION</li> <li>• CONVEX SOLDERING SPOT</li> <li>• SOLDER BALLS</li> <li>• SOLDER SCRAPS</li> </ul>	
	4.DISPLAY ON (ALL ON)	<ul style="list-style-type: none"> <li>• LIGHT LINE</li> </ul>	

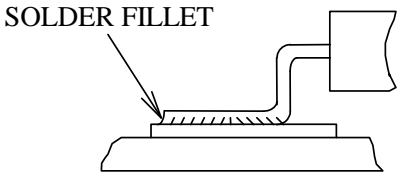
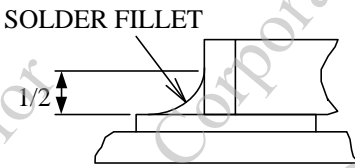
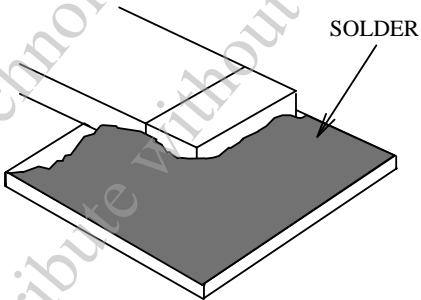
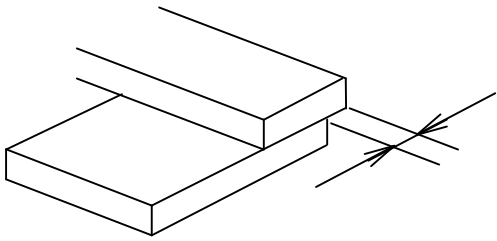
NOTE :

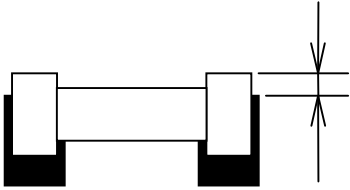
REGIONS OUTSIDE OF VIEWING AREA : ANY CONTAMINATION OR COSMETIC BLEMISHES THAT ARE LOCATED OUTSIDE OF THE LCM'S VIEWING AREA ARE CONSIDERED AS ACCEPTABLE, REGARDLESS OF THE CONDITION'S SIZE, LOCATION AND QUANTITY.

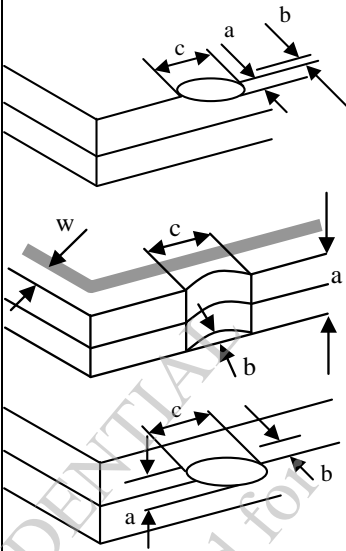
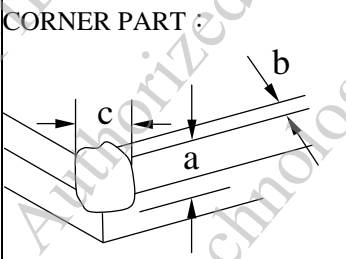
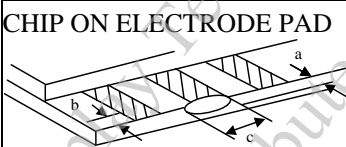
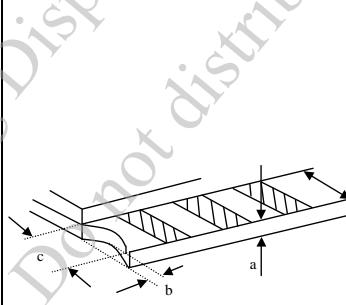
12.3.2 MODULE DEFECTS CLASSIFICATION

NO.	ITEM	CRITERIA												
1	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC												
2	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC												
3	DOT DEFECT	(1) INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS. (2) <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>ITEMS</th> <th>ACCEPTABLE COUNT</th> </tr> </thead> <tbody> <tr> <td>BRIGHT DOT</td> <td><math>N \leq 2</math></td> </tr> <tr> <td>DARK DOT</td> <td><math>N \leq 3</math></td> </tr> <tr> <td>TOTAL BRIGHT AND DARK DOTS</td> <td><math>N \leq 4</math></td> </tr> </tbody> </table> <p>NOTE :</p> <p>1. THE DEFINITION OF DOT : THE SIZE OF A DEFECTIVE DOT OVER 1/2 OF WHOLE DOT IS REGARDED AS ONE DEFECTIVE DOT.</p> <p>2. BRIGHT DOT : DOTS APPEAR BRIGHT AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER BLACK PATTERN.</p> <p>3. DARK DOT : DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURE.</p>	ITEMS	ACCEPTABLE COUNT	BRIGHT DOT	$N \leq 2$	DARK DOT	$N \leq 3$	TOTAL BRIGHT AND DARK DOTS	$N \leq 4$				
ITEMS	ACCEPTABLE COUNT													
BRIGHT DOT	$N \leq 2$													
DARK DOT	$N \leq 3$													
TOTAL BRIGHT AND DARK DOTS	$N \leq 4$													
4	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	<table border="1" style="margin-left: 20px;"> <thead> <tr> <th>LENGTH : L</th> <th>WIDTH : W</th> <th>PERMISSIBLE NO.</th> </tr> </thead> <tbody> <tr> <td><math>L \leq 0.3</math></td> <td><math>W \leq 0.05</math></td> <td>IGNORE</td> </tr> <tr> <td><math>0.3 &lt; L \leq 2.5</math></td> <td><math>0.05 &lt; W \leq 0.1</math></td> <td>4</td> </tr> <tr> <td><math>2.5 &lt; L</math></td> <td><math>0.1 &lt; W</math></td> <td>NONE</td> </tr> </tbody> </table> <p>WIDTH : W mm, LENGH : L mm</p>	LENGTH : L	WIDTH : W	PERMISSIBLE NO.	$L \leq 0.3$	$W \leq 0.05$	IGNORE	$0.3 < L \leq 2.5$	$0.05 < W \leq 0.1$	4	$2.5 < L$	$0.1 < W$	NONE
LENGTH : L	WIDTH : W	PERMISSIBLE NO.												
$L \leq 0.3$	$W \leq 0.05$	IGNORE												
$0.3 < L \leq 2.5$	$0.05 < W \leq 0.1$	4												
$2.5 < L$	$0.1 < W$	NONE												
5	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	<table border="1" style="margin-left: 20px;"> <thead> <tr> <th>AVERAGE DIAMETER (mm): D</th> <th>NUMBER OF PIECES PERMITTED</th> </tr> </thead> <tbody> <tr> <td><math>D \leq 0.15</math></td> <td>IGNORE</td> </tr> <tr> <td><math>0.15 &lt; D \leq 0.5</math></td> <td>4</td> </tr> <tr> <td><math>0.5 &lt; D</math></td> <td>NONE</td> </tr> </tbody> </table> <p>NOTE : DIAMETER <math>D=(a+b)/2</math></p> 	AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	$D \leq 0.15$	IGNORE	$0.15 < D \leq 0.5$	4	$0.5 < D$	NONE				
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$D \leq 0.15$	IGNORE													
$0.15 < D \leq 0.5$	4													
$0.5 < D$	NONE													

NO.	ITEM	CRITERIA		
			AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED
6	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS	BUBBLE ON THE POLARIZER	$D \leq 0.25$	IGNORE
			$0.25 < D \leq 0.5$	$N \leq 5$
			$0.5 < D$	NOTE
		SURFACE STATUS	$D < 0.1 \text{ mm}$	IGNORE
			$0.1 < D \leq 0.3\text{mm}$	$N \leq 3$
		CF FAIL / SPOT	$D < 0.1 \text{ mm}$	IGNORE
$0.1 < D \leq 0.3\text{mm}$	$N \leq 3$			
		<p>NOTE : (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA.</p> <p>(2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON.</p> <p>(3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING.</p> <p>AVERAGE DIAMETER (D)=(a+b)/2</p> 		
7	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW		
8	MURA ON DISPLAY	IT'S OK IF MURA IS SLIGHT VISIBLE THROUG 6% ND FILTER		
9	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.		
10	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.		
11	PCB	<p>(1)THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES.</p> <p>(2)NO OXIDATION OR CONTAMINATION PCB TERMINALS.</p> <p>(3)PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS.</p> <p>(4)THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART.</p> <p>(5)IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.</p>		

NO.	ITEM	CRITERIA
12	SOLDERING	<p>(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICIENT SOLDER</p> <p>(a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD</p>  <p>(b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING</p>  <p>· SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED</p>  <p>(3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE</p> 

NO.	ITEM	CRITERIA
12	SOLDERING	<p>(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE</p>  <p>(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB.                      (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE.                      (6)NO RESIDUE OR SOLDER BALLS ON PCB.                      (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.</p>
13	BACKLIGHT	<p>(1)NO LIGHT                      (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION                      (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS.                      (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.</p>
14	GENERAL APPEARANCE	<p>(1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP.                      (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP.                      (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT.                      (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS.                      (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER.                      (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR.                      (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED.                      (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET.                      (9)LCD PIN LOOSE OR MISSING PINS.                      (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET.                      (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET.                      (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.</p>

NO.	ITEM	CRITERIA									
15	CRACKED GLASS	<p>THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE</p> <p>GENERAL GLASS CHIP :</p>  <table border="1"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t/2</math></td> <td>&lt; VIEWING AREA</td> <td><math>\leq 1/8X</math></td> </tr> <tr> <td><math>t/2 &gt; , \leq 2t</math></td> <td><math>\leq W/2</math></td> <td><math>\leq 1/8X</math></td> </tr> </tbody> </table> <p>*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS</p>	a	b	c	$\leq t/2$	< VIEWING AREA	$\leq 1/8X$	$t/2 > , \leq 2t$	$\leq W/2$	$\leq 1/8X$
		a	b	c							
		$\leq t/2$	< VIEWING AREA	$\leq 1/8X$							
		$t/2 > , \leq 2t$	$\leq W/2$	$\leq 1/8X$							
<p>CORNER PART :</p>  <table border="1"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t/2</math></td> <td>&lt; VIEWING AREA</td> <td><math>\leq 1/8X</math></td> </tr> <tr> <td><math>&gt; t/2 , \leq 2t</math></td> <td><math>\leq W/2</math></td> <td><math>\leq 1/8X</math></td> </tr> </tbody> </table> <p>*W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS</p>	a	b	c	$\leq t/2$	< VIEWING AREA	$\leq 1/8X$	$> t/2 , \leq 2t$	$\leq W/2$	$\leq 1/8X$		
a	b	c									
$\leq t/2$	< VIEWING AREA	$\leq 1/8X$									
$> t/2 , \leq 2t$	$\leq W/2$	$\leq 1/8X$									
<p>CHIP ON ELECTRODE PAD</p>  <table border="1"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t</math></td> <td><math>\leq 0.5\text{mm}</math></td> <td><math>\leq 1/8X</math></td> </tr> </tbody> </table> <p>* X=LCD SIDE WIDTH t=GLASS THICKNESS</p>	a	b	c	$\leq t$	$\leq 0.5\text{mm}$	$\leq 1/8X$					
a	b	c									
$\leq t$	$\leq 0.5\text{mm}$	$\leq 1/8X$									
 <table border="1"> <thead> <tr> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td><math>\leq t</math></td> <td><math>\leq 1/8X</math></td> <td><math>\leq L</math></td> </tr> </tbody> </table> <p>*X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH</p> <p>①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS</p> <p>②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DAMAGED</p>	a	b	c	$\leq t$	$\leq 1/8X$	$\leq L$					
a	b	c									
$\leq t$	$\leq 1/8X$	$\leq L$									



12.4 RELIABILITY TEST

12.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C , 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED )	<p>THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION:</p> <p>The diagram illustrates a thermal shock cycle. It starts at -30°C, ramps up to +80°C over 30 minutes, dwells at +80°C for 3 minutes, ramps down to -30°C over 30 minutes, and dwells at -30°C for 3 minutes. This sequence is labeled as '1 cycle'.</p>
7	ESD (ELECTROSTATIC DISCHARGE ) (NOT OPERATED )	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

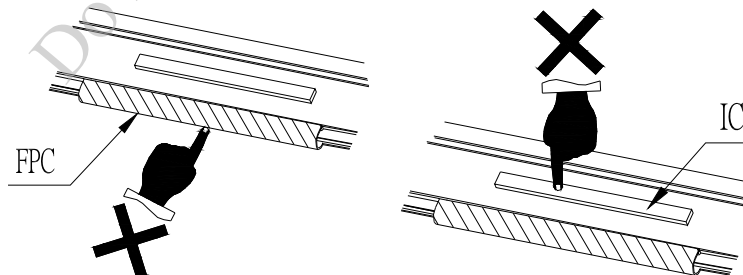
## 12.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 12.5 , STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

## 12.6 OPERATION

- 12.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 12.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLA DISCOLOR . WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 12.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 12.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.  
IF ABOVE SEQUENCE IS NOT FOLLOWED , CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM .
- 12.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!  
DO NOT STRESS FPC AND IC ON THE MODULE!



## 12.7 NOTICE

- 12.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 12.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 12.7.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 12.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 12.7.5 DON'T GIVE EXTERNAL SHOCK.
- 12.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 12.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.  
WHEN THE LIQUID IS ATTACHED TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 12.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 12.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS AND SOLVENT.
- 12.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 12.7.11 REWIRING: NO MORE THAN 3 TIMES.